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☐ 1. Document ID: WO 9967760 A1, EP 1093646 A1, AU 9947008 A, US 6212171 B1

L15: Entry 1 of 1

File: DWPI

Dec 29, 1999

DERWENT-ACC-NO: 2000-237161

DERWENT-WEEK: 200124

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TITLE: Apparatus for determining serial bus network gap count by transmitting packet from first leaf node to second leaf node and back and determining round- trip delay time between nodes in network

INVENTOR: HAUCK, J; LAFOLLETTE, D

PATENT-ASSIGNEE:

ASSIGNEE

INTEL CORP

CODE

ITLC

PRIORITY-DATA: 1998US-0102097 (June 22, 1998)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
<u>WO 9967760 A1</u>	December 29, 1999	E	041	G08C015/00
<u>EP 1093646 A1</u>	April 25, 2001	E	000	G08C015/00
<u>AU 9947008 A</u>	January 10, 2000		000	G08C015/00
<u>US 6212171 B1</u>	April 3, 2001		000	H04L012/40

DESIGNATED-STATES: AE AL AM AT AU AZ BA BB BG BR BY CA CH CN CU CZ DE DK EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MD MG MK MN MW MX NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR TT UA UG UZ VN YU ZA ZW AT BE CH CY DE DK EA ES FI FR GB GH GM GR IE IT KE LS LU MC MW NL OA PT SD SE SL SZ UG ZW AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU MC NL PT SE

APPLICATION-DATA:

PUB-NO	APPL-DATE	APPL-NO	DESCRIPTOR
WO 9967760A1	June 22, 1999	1999WO-US13930	
EP 1093646A1	June 22, 1999	1999EP-0930471	
EP 1093646A1	June 22, 1999	1999WO-US13930	
EP 1093646A1		<u>WO 9967760</u>	Based on
AU 9947008A	June 22, 1999	1999AU-0047008	
AU 9947008A		<u>WO 9967760</u>	Based on
US 6212171B1	June 22, 1998	1998US-0102097	

INT-CL (IPC): G08 C 15/00; H04 L 12/40

RELATED-ACC-NO: 2000-181992

ABSTRACTED-PUB-NO: US 6212171B

BASIC-ABSTRACT:

NOVELTY - Method finds round-trip delay time for sending packet from 1st leaf node to 2nd and back over communications path between nodes for each pair of leaf nodes in network. Each path's maximum delay time is calculated (304), and each node's latency delay time (306) is found. Longest maximum round-trip and longest node latency delay times are selected for the network and a gap count calculated with these times.

USE - As an apparatus for determining gap count for a serial bus network.

ADVANTAGE - Determines an improved gap count for a given network topology that solves the on-going problems associated with determining serial bus network gap counting.

DESCRIPTION OF DRAWING(S) - The drawing shows a block flow diagram of the steps performed by a measuring node.

Calculating each path's maximum delay time 304

Calculating each node's latency delay time 306

ABSTRACTED-PUB-NO:

WO 9967760A

EQUIVALENT-ABSTRACTS:

NOVELTY - Method finds round-trip delay time for sending packet from 1st leaf node to 2nd and back over communications path between nodes for each pair of leaf nodes in network. Each path's maximum delay time is calculated (304), and each node's latency delay time (306) is found. Longest maximum round-trip and longest node latency delay times are selected for the network and a gap count calculated with these times.

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Calculating each path's maximum delay time 304

Calculating each node's latency delay time 306

CHOSEN-DRAWING: Dwg.3/7

TITLE-TERMS: APPARATUS DETERMINE SERIAL BUS NETWORK GAP COUNT TRANSMIT PACKET FIRST LEAF NODE SECOND LEAF NODE BACK DETERMINE ROUND TRIP DELAY TIME NODE NETWORK

DERWENT-CLASS: W01

EPI-CODES: W01-A06E;

SECONDARY-ACC-NO:

Non-CPI Secondary Accession Numbers: N2000-177871

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	Pub	Drawings	Clip	Image
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